

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	09602279	Docket Number	MIPS.0168-00-US
Filed	6/23/2000	Group Art Unit	2182
Examiner	TAMMARA R. PEYTON	Customer No.	23669
Application Title	BACKGROUND MEMORY MANAGER THAT DETERMINES IF DATA STRUCTURES FITS IN MEMORY WITH MEMORY STATE TRANSACTIONS MAP		
First Named Inventor	MARIO D. NEMIROVSKY		

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Mail Stop **AMENDMENT**
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

Respectfully submitted,
HUFFMAN LAW GROUP, P.C.

/James W. Huffman/

By: _____

Jim Huffman
Registration No. 35,549
Tel: (719) 475-7103

5/22/07

Date: _____

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
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				Filing Date	6/23/2000
				First Named Inventor	Mario D. Nemirovsky
				Group Art Unit	2182
				Examiner Name	Tammara R. Peyton
Sheet	1	of	3	Attorney Docket Number	MIPS.0168-00-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		5465331	11-07-1995	Yang et al.	
		5521916	05-28-1996	Choudhury et al.	
		5619497	04-08-1997	Gallagher et al.	
		5708814	01-13-1998	Short et al.	
		5724565	03-03-1998	Dubey et al.	
		5737525	04-07-1998	Picazo et al.	
		5784649	07-21-1998	Begur et al.	
		5809321	09-15-1998	Hansen et al.	
		6219339	04-17-2001	Doshi et al.	
		6381242	04-30-2002	Maher, III et al.	
		6438135	08-20-2002	Tshrijie Tzeng	
		6460105	10-01-2001	Jones et al.	
		6483804	11-19-2002	Muller et al.	
		6523109	02-18-2003	Stephan G. Meier	
		6529515	03-04-2003	Raz et al.	
		6535905	03-18-2003	Kalafatis et al.	
		6625808	09-23-2003	David R. Tarditi	
		6640248	10-28-2003	Jacob W. Jorgensen	
		6738371	05-18-2004	Lawrence Ayres	
		6738378	05-18-2004	Tuck, III et al.	
		6813268	11-02-2004	Kalkunte et al.	
		20010004755	06-21-2001	Levy et al.	
		20010024456	09-27-2001	Zaun et al.	
		20010043610	11-22-2001	Nemirovsky et al.	

Examiner Signature		Date Considered	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**

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		Number-Kind Code ² (if known)			
		20010052053	12-13-2001	Nemirovsky et al.	
		20020016883	02-07-2002	Musoll et al.	
		20020049964	04-25-2002	Takayama et al.	
		20020071393	06-13-2002	Enrique Musoll	
		20020083173	06-27-2002	Musoll et al.	
		7032226	04-18-2006	Nemirovsky et al.	
		20060153197	07-13-2006	Nemirovsky et al.	
		20060159104	07-20-2006	Nemirovsky et al.	
		6247040	06-12-2001	Born et al.	
		20060215679	09-28-2006	Musoll et al.	
		20060215670	09-28-2006	Sampath et al.	
		20060225080	10-05-2006	Nemirovsky et al.	
		20070074014	03-29-2007	Musoll et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
		WO 03/005645A2	06-25-2002	Sampath et al.		

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Sheet	3	of	3	Attorney Docket Number	MIPS.0168-00-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		MELVIN ET AL., "Extended Instruction Set for a Packet Processing Applications," July 5, 2001, Disclosure Document #496559, USPTO	
		MUSOLL ET AL., "Hardware Algorithm for Allocating and De-Allocating Consecutive Blocks of Memory," April 3, 2001, Disclosure Document #491557, USPTO	
		MUSOLL ET AL., "Mechanism to Overflow Packets to a Software Controlled Memory When They Do Not Fit into a Hardware Controlled Memory," July 3, 2001, Disclosure Document #496391, USPTO	
		MUSOLL ET AL., "Mechanism for Allowing a Limited Packet Head and/or Tail Growth Without Moving the Packet to a Different Memory Location," April 16, 2001, Disclosure Document #492429, USPTO	
		MUSOLL ET AL., "Mechanism to Activate a Context When No Stream is Running in a Multi-Streaming Processing Core," April 16, 2001, Disclosure Document #492431, USPTO	
		MUSOLL, ENRIQUE, "Functional Validation of a Packet Management Unit," May 18, 2001, Disclosure Document #429011, USPTO	
		SAMPATH ET AL., "Mechanism to Un-speculatively Pre-fetch Instructions from the Thread Associated to a Packet," April 2, 2001, Disclosure Document #491527, USPTO	
		YAMAMOTO, WAYNE. An Analysis of Multistreamed, Superscalar Processor Architectures. University of California Santa Barbara Dissertation. December 1995. Santa Barbara, US.	
		YAMAMOTO ET AL. "Increasing Superscalar Performance Through Multistreaming." Parallel Architectures and Compilation Techniques (PACT '95). 1995	
		The PowerPC Architecture: A Specification for a New Family of RISC Processors. 2 nd Ed. May 1994. pp. 70-72. Morgan Kaufmann. San Francisco, US.	
		MC68020 32-Bit Microprocessor User's Manual. 3 rd Ed.. 1989. pp. 3-125, 3-126, and 3-127. Prentice Hall, NJ, US.	

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